

APPROVAL SHEET

WW25K, WW25J

±1%, ±5%

Metal Ultra low ohm power chip resistors

Size 2512, WW25K 2W, WW25J 1W

Metal Current Sensing Type

RoHS Exemption free and Lead free products

Halogen free

Low EMF



*Contents in this sheet are subject to change without prior notice.

FEATURE

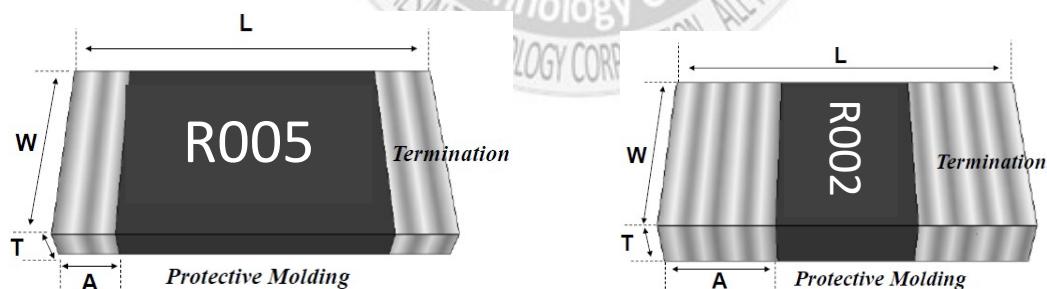
1. Ultra low and stable TCR performance
2. High power rating and low EMF $<+/-3\mu\text{V}/^\circ\text{C}$
3. High reliability and stability
4. Reduced size of final equipment
5. RoHS Exemption free and Lead free products
6. Inductance below 1nH

APPLICATION

- Power supply
- PDA
- Digital meter
- Computer
- Automotives
- Battery charger
- DC-DC power converter

DESCRIPTION

The resistors are constructed in a high grade low resistive metal body. The resistive layer is covered with a protective coat and printed a resistance marking code over it. Finally, the two external end terminations are added. For ease of soldering the outer layer of these end terminations is a lead-free solder.



Item	Protective Molding	Resistive Element	Internal Terminal	External Terminal
Material	Resin	Alloy Metal	Copper	Solder

Fig 1. Construction of Chip-R

QUICK REFERENCE DATA

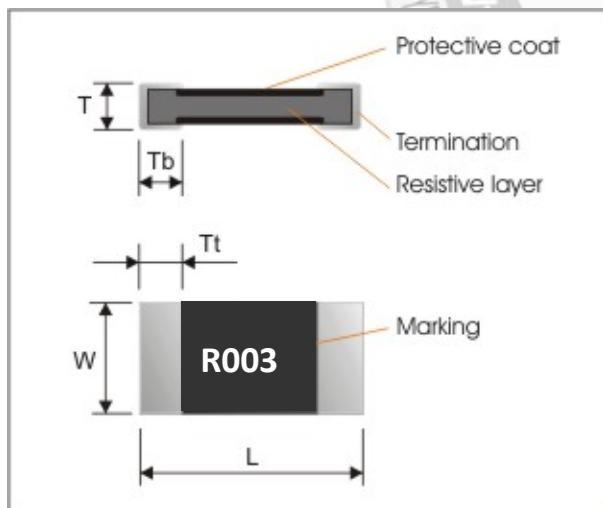
Item	General Specification	General Specification
Series No.	WW25J	WW25K
Size code	2512 (6432)	2512 (6432)
Resistance Tolerance	$\pm 5\%$; $\pm 1\%$	$\pm 5\%$; $\pm 1\%$
Resistance Value	0.001 Ω , 0.002 Ω , 0.003 Ω , 0.004 Ω , 0.005 Ω , 0.006 Ω , 0.007 Ω , 0.008 Ω , 0.009 Ω , 0.010 Ω , 0.015 Ω , 0.020 Ω , 0.025 Ω	0.001 Ω , 0.002 Ω , 0.003 Ω , 0.004 Ω , 0.005 Ω , 0.006 Ω , 0.007 Ω , 0.008 Ω , 0.009 Ω , 0.010 Ω , 0.015 Ω , 0.020 Ω , 0.025 Ω
TCR (ppm/ $^{\circ}$ C)	± 70	± 70
Max. dissipation at $T_{amb}=70^{\circ}$ C	1W	2W
Operation temperature	-55/+170 $^{\circ}$ C	-55/+170 $^{\circ}$ C

Note :

1. This is the maximum voltage that may be continuously supplied to the resistor element, see "IEC publication 60115-8"
2. Max. Operation Voltage : So called RCWV (Rated Continuous Working Voltage) is determined by

$$RCWV = \sqrt{\text{Rated Power} \times \text{Resistance Value}} \text{ or Max. RCWV listed above, whichever is lower.}$$

MECHANICAL DATA



Symbol	R001, R002	R003 ~ R020
L	6.40 ± 0.25	6.20 ± 0.25
W	3.25 ± 0.20	3.25 ± 0.20
T	0.75 ± 0.20	0.65 ± 0.20
Tt	2.00 ± 0.20	0.80 ± 0.20
Tb	2.00 ± 0.20	0.80 ± 0.20

MARKING

TOP : Marking. (4 Digits marking to identify the resistance value.)



R005=5mΩ , R020=20mΩ



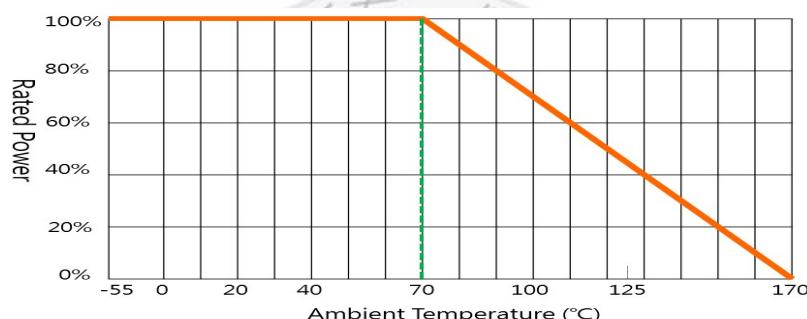
R001=1mΩ ; R002=2mΩ

FUNCTIONAL DESCRIPTION

Derating curve

The power that the resistor can dissipate depends on the operating temperature; see Fig.2

Operating Temperature Range: -55 to +170°C



Recommend Solder Pad Dimensions :



Type	W	D	L
3~25 mOhm	3.70	1.60	7.60
1-2 mOhm	4.00	3.00	7.30

MOUNTING

Due to their rectangular shapes and small tolerances, Surface Mountable Resistors are suitable for handling by automatic placement systems.

Chip placement can be on ceramic substrates and printed-circuit boards (PCBs).

Electrical connection to the circuit is by individual soldering condition.

The end terminations guarantee a reliable contact.

SOLDERING CONDITION

The robust construction of chip resistors allows them to be completely immersed in a solder bath of 260°C for 10 seconds. Therefore, it is possible to mount Surface Mount Resistors on one side of a PCB and other discrete components on the reverse (mixed PCBs).

Surface Mount Resistors are tested for solderability at 235°C during 3 seconds. The test condition for no leaching is 260°C for 30 seconds. Typical examples of soldering processes that provide reliable joints without any damage are given in Fig 3.

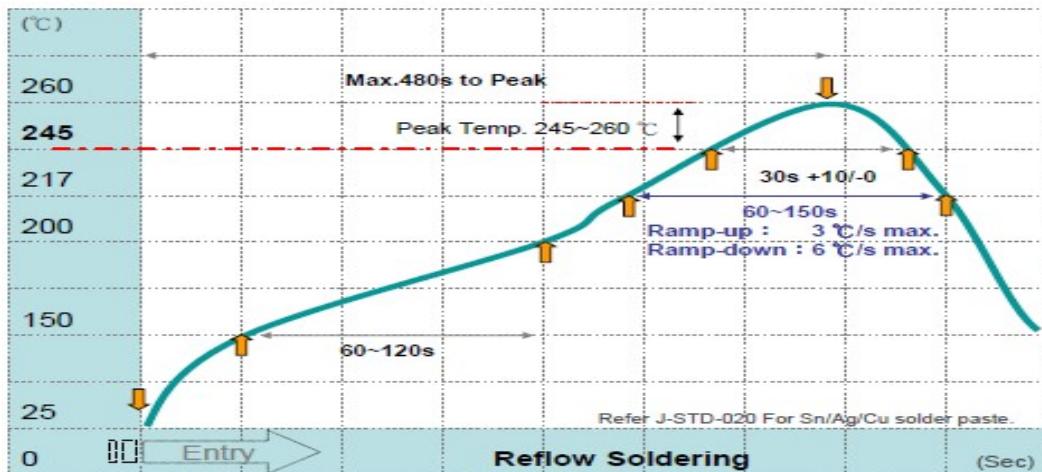


Fig 3. Infrared soldering profile for Chip Resistors WW25

CATALOGUE NUMBERS

The resistors have a catalogue number starting with .

WW25	K	R010	F	T	L
Size code WW25 : 2512	Type code K : 2512 2W J : 2512 1W Metal strip Low EMF	Resistance code R is first digit followed by 3 significant digits. 0.010Ω = R010	Tolerance J : ±5% F : ±1%	Packaging code T : 7" reeled in tape	Termination code L = Sn base (lead free)

WW25J/ WW25K: 4,000 pcs per PC Tape reel

TEST AND REQUIREMENTS (JIS C 5201-1 : 1998)

Essentially all tests are carried out according to the schedule of IEC publication 115-8, category **LCT/UCT**/56(rated temperature range : Lower Category Temperature, Upper Category Temperature; damp heat, long term, 56 days). The testing also meets the requirements specified by EIA, EIAJ and JIS.

The tests are carried out in accordance with IEC publication 68, "Recommended basic climatic and mechanical robustness testing procedure for electronic components" and under standard atmospheric conditions according to IEC 60068-1, subclause 5.3. Unless otherwise specified, the following value supplied :

Temperature: 15°C to 35°C.

Relative humidity: 45% to 75%.

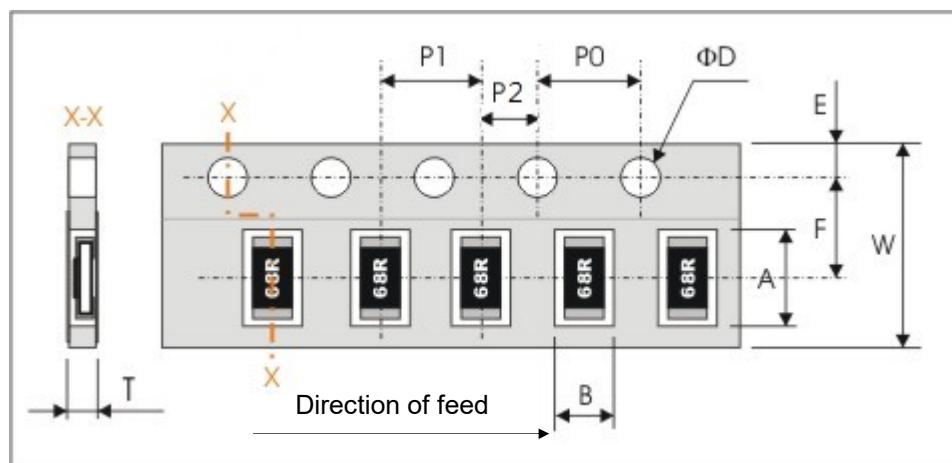
Air pressure: 86kPa to 106 kPa (860 mbar to 1060 mbar).

All soldering tests are performed with mildly activated flux.

TEST	PROCEDURE	REQUIREMENT
Temperature Coefficient of Resistance(T.C.R) Clause 4.8	Natural resistance change per change in degree centigrade. $\frac{R_2 - R_1}{R_1(t_2 - t_1)} \times 10^6 \text{ (ppm/}^\circ\text{C)} \quad t_1 : 20^\circ\text{C} + 5^\circ\text{C} - 1^\circ\text{C}$ R ₁ : Resistance at reference temperature R ₂ : Resistance at test temperature	Refer to "QUICK REFERENCE DATA"
Short time overload (S.T.O.L) Clause 4.13	Permanent resistance change after a 5 second application of 5 times rated power specified in the above list,	no visible damage J: ΔR/R max. ±(2%+0.5mΩ) F: ΔR/R max. ±(1%+0.5mΩ)
Resistance to soldering heat (R.S.H) Clause 4.18	Un-mounted chips completely immersed for 10±1second in a SAC solder bath at 270°C±5°C	no visible damage J: ΔR/R max. ±(1%+0.5mΩ) F: ΔR/R max. ±(0.5%+0.5mΩ)
Solderability Clause 4.17	Un-mounted chips completely immersed for 3±0.5 second in a SAC solder bath at 235°C±2°C	good tinning (>95% covered) no visible damage
Temperature cycling Clause 4.19	30 minutes at -55°C±3°C, 2~3 minutes at 25°C+2°C-1°C, 30 minutes at +155°C±3°C, 2~3 minutes at 25°C+2°C-1°C, total 5 continuous cycles	no visible damage J: ΔR/R max. ±(1%+0.5mΩ) F: ΔR/R max. ±(0.5%+0.5mΩ)
Load life (endurance) Clause 4.25	70±2°C, 1000 hours, loaded with RCWV or Vmax, 1.5 hours on and 0.5 hours off	no visible damage J: ΔR/R max. ±(3%+0.5mΩ) F: ΔR/R max. ±(1%+0.5mΩ)
Load life in Humidity Clause 4.24	1000 hours, at rated continuous working voltage in humidity chamber controller at 40°C±2°C and 90~95% relative humidity, 1.5hours on and 0.5 hours off	no visible damage J: ΔR/R max. ±(3%+0.5mΩ) F: ΔR/R max. ±(1%+0.5mΩ)
Insulation Resistance Clause 4.6	Resistance between termination and overcoat. test voltage 100+/-15V	Min. 1Gohm
Bending Clause 4.33	Resistance change after bended 3mm on the 90mm PCB. 2mm for 1206/ 2512!	no visible damage J: ΔR/R max. ±(1%+0.5mΩ) F: ΔR/R max. ±(0.5%+0.5mΩ)
Adhesion Clause 4.32	Pressurizing force: 1Kg, Test time: 60±1sec.	No remarkable damage or removal of the terminations

PACKAGING

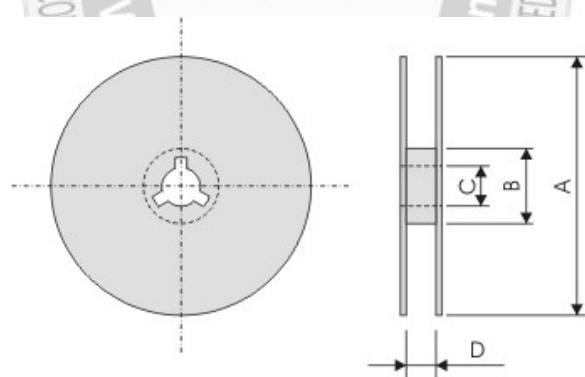
Plastic Tape specifications (unit :mm)



Series No.	A	B	W	F	E
WW25J/ WW25K	6.75 ± 0.20	3.50 ± 0.20	12.00 ± 0.30	5.50 ± 0.10	1.75 ± 0.10

Series No.	P1	P0	P2	ΦD	T
WW25J/ WW25K	4.00 ± 0.10	4.00 ± 0.10	2.00 ± 0.10	$\Phi 1.50^{+0.1}_{-0.0}$	1.2 ± 0.20

Reel dimensions



Size	A	B	C	D
2512	$\Phi 178.0 \pm 2.0$	$\Phi 60.0 \pm 1.0$	13.0 ± 0.5	13.8 ± 1.5

Taping Qty: WW25J/ WW25K: 4,000pcs per reel